

STORAGE CLASS MEMORY (SCM)

WHAT IS SCM?

- Storage Class Memory (SCM) is a way of connecting Emerging Non-Volatile Memories (NVM) into Parallel Memory Interface.
- What are Emerging Non-Volatile Memory technologies?
 - Phase Change Memory (PCM)
 - Resistive RAM (ReRAM)
 - Magneto Resistive RAM (MRAM)
 - Ferro Electric RAM (FeRAM)
 - 3D X-Point
 - Etc.

By connecting Emerging NVM into parallel memory interface and directly connecting SCM into host memory controller / host CPU this new architecture will provide Highest Bandwidth, and Lowest Latency Storage solution to the end applications.

WHO WOULD DO IT / USE IT?

We believe Intel, Micron, Samsung, SK Hynix, Toshiba / Kioxia, WD/HGST/SanDisk, Rambus, Microsoft, Kingston, Viking, and SMART would be among technology developer.

And IBM, HPE, Dell/EMC, Oracle, NetAPP, Google, Facebook would be among end user of this technology.

Disclaimer:

Xitore, Inc. is not a member of either JEDEC, SNIA, Open CAPI, Gen-Z, or CXL organizations since its inception in 2014. The company is a technology licensing company with a comprehensive patent portfolio in Persistent and Storage Class Memory space. For more information visit our website at: [Xitore IP Licensing](#)

WHAT IS NEEDED FOR SCM?

- Parallel Memory Interface comprised of standards such as (i.e.):
 - JEDEC (DDR4, DDR5, etc.)
 - Open CAPI
 - Gen-Z
 - CXL
- Byte Addressable and/or Block Addressable
- Optional Volatile Memory technologies such as:
 - SRAM
 - DRAM
 - MRAM
 - Etc.
- Emerging NVM
- On-board SCM Controller
- Optional: Driver defined by SNIA