

## PERSISTENT MEMORY (PM) & STORAGE CLASS MEMORY (SCM)

### JEDEC

- JEDEC is finalizing its DDR5 & NVDIMM-P specification for public release, expected Q3'2020.
- Few companies start shipping & sampling their Persistent Memory (PM) Byte-addressable & Block-addressable products to their end customers.
- Several companies awaiting for JEDEC specification to be publically released prior to shipping their Storage Class Memory (SCM) as JEDEC compliant and Compatible solution to their end customers
- Industry is moving toward new PM and SCM adaptation to revolutionize existing and new systems as we know it.
- JEDEC's new specification will cover:
  - NVDIMM-P Byte Addressable
  - NVDIMM-P Block Addressable
  - Emerging Non-Volatile Memory connection to memory bus (Storage Class Memory "SCM")
  - Persistent Memory (PM) product families

### SNIA

- ❖ SNIA is published a set of specifications for how applications to access the new type of storage connected into system memory bus.
- ❖ These comprise of how application should access NVDIMM/PM/SCM as block addressable solution using various OS libraries, NVDIMM Driver, PM Driver, SCM Driver
- ❖ Along with Direct Application Access mode to perform load/store transactions after application open access as a file via OS libraries, NVDIMM Driver, PM Driver, SCM Driver for fast direct access (DAX)
- ❖ Both Windows & Linux are providing various solutions in their Operating Systems (OS) to take advantage of NVDIMM, PM, and SCM solutions today.

#### Disclaimer:

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